

Amendments to the Specification:

Please replace the paragraph, beginning at page 28, line 7, with the following rewriting paragraph:

FIG. 1 is a circuit diagram of a radio communication apparatus related to a first embodiment of the present invention. In this diagram, reference numeral 101 denotes a single-phase input-output antenna corresponding to an first antenna of the present invention as an example, 102 denotes a duplexer (antenna sharing apparatus) of which transmission input terminal is a single-phase input type, antenna input-output terminal is a single-phase input-output type, and receiving output terminal is a balanced output type as an example of a first duplexer of the present invention, 103 denotes a receiving circuit of differential input as an example of a first receiving apparatus of the present invention connected to the balanced output terminal and having a circuit in which a gain of a signal of a differential component is higher than that of a signal of an in-phase component, or a loss of the signal of the differential component is lower than that of the signal of the in-phase component, and 104 denotes a single-phase output transmitting circuit as an example of a first-transmitting apparatus of the present invention of outputting a transmitting signal in a first frequency band. The duplexer 102 outputs a signal in a frequency band (corresponding to a second frequency band of the present invention) of the receiving signal inputted from an antenna input-output terminal as a differential signal to the receiving output terminal, and outputs a part of the signal in the frequency band (corresponding to a first frequency band of the present invention) of a transmitting signal inputted from the transmission input terminal (transmitting signal leak) as an in-phase signal from the receiving output terminal.

Please replace the paragraph, beginning at page 44, line 11, with the following rewriting paragraph:

FIG. 11 is a circuit diagram of the radio communication apparatus related to a second embodiment of the present invention. In FIG. 11, the same components as those shown in FIG. 1 are given the same symbols, and a description thereof will be omitted. In FIG. 11, reference numeral 902 denotes a duplexer of which terminals are single-phase input-output, and 905 denotes a phase shifter of single-phase input and balanced output which outputs the signals in the frequency band of the receiving signals as the differential signals and outputs the signals in the frequency band of the transmitting signals as the in-phase signals. Here, the first-duplexer

of the present invention described in the first embodiment is corresponding as an example to the duplexer 902 and the phase shifter 905.

Please replace the paragraph, beginning at page 50, line 1, with the following rewritten paragraph:

FIG. 13 is a circuit diagram of the radio communication apparatus related to a third embodiment of the present invention. In FIG. 13, the same components as those shown in FIG. 1 are given the same symbols, and a description thereof will be omitted. In FIG. 13, ~~an antenna 1101+ and an antenna 1101- are examples~~ is an example of second and third antennas one antenna of the present invention having a first feeding point of feeding a receiving signal and also having two or more polarized waves, antenna 1101 - is an example of another antenna of the present invention placed along with the one antenna and having a second feeding point of feeding the receiving signal and also having two or more polarized waves, and a duplexer 1102 is an example of a ~~second~~ duplexer of the present invention comprising separate terminals, that is, the transmission input terminal is the single-phase input type, the receiving output terminal is the balanced output type, and the antenna input-output terminal is the balanced input-output type.

Please replace the paragraph, beginning at page 50, line 13, with the following rewriting paragraph:

As for the radio communication apparatus shown in FIG. 13, the radio frequency signal transmitted from the base station is received by the antennas 1101+ and 1101- as in FIG. 1 and is then inputted to the duplexer 1102. Furthermore, the signal outputted from the duplexer 1102 is inputted to the receiving circuit 103, where it is converted into the base band signal. The predetermined signal processing is performed to the transmitting base band signal and then it is inputted to the transmitting circuit 204 as an example of a second transmitting apparatus of the present invention of outputting a transmitting signal, where it is converted into the radio frequency and amplified to the predetermined sending power to be inputted to the duplexer 1102. Furthermore, it is constituted so that this signal is outputted from the duplexer 1102 to be sent to the base station from the antennas 1101+ and 1101-. And a part of the transmitting signal inputted to the duplexer 1102 leaks to the receiving circuit 103.

Please replace the paragraph, beginning at page 59, line 15, with the following rewritten paragraph:

FIG. 17 is a circuit diagram of the radio communication apparatus related to a third embodiment of the present invention. In FIG. 17, the same components as those shown in FIG. 1 are given the same symbols, and a description thereof will be omitted. In FIG. 17, ~~an~~ antenna 1401+ is another example of ~~the second~~said one antenna of the present invention and ~~an~~ antenna 1401- is another example of ~~the third~~said another antenna thereof which are described in the third embodiment. A duplexer 1402 is another example of the ~~second~~ duplexer of the present invention in which the transmission input terminal is the single-phase input type, the receiving output terminal is the balanced output type, and the antenna input-output terminal is the balanced input-output type.

Please replace the paragraph, beginning at page 66, line 21, with the following rewriting paragraph:

FIG. 20 is a circuit diagram of the radio communication apparatus related to a fifth embodiment of the present invention. In FIG. 20, an antenna 3301 is a single-phase input-output antenna corresponding to the ~~first~~ antenna of the present invention described in the first embodiment as another example, a duplexer 3302 is corresponding to the ~~third~~ duplexer of the present invention as an example, and its transmission input terminal is the balanced input type, its antenna input-output terminal is the single-phase input-output type, and its receiving output terminal is the single-phase output type. A receiving circuit 3303 has an input terminal of the single-phase type, and a transmitting circuit 3304 has as an output terminal of the balanced type and outputs differential transmitting signals. FIG. 24 shows a configuration example of the duplexer 3302.

Please replace the paragraph, beginning at page 67, line 10, with the following rewriting paragraph:

In FIG. 24, a phase shifter 2201+ is corresponding to a ~~seventh~~first phase shifter of the present invention, a phase shifter 2201- is corresponding to ~~an eighth~~ a second phase shifter of the present invention, a phase shifter 2202+ is corresponding to a ~~ninth~~third phase shifter of the present invention, a phase shifter 2202- is corresponding to a ~~tenth~~fourth phase shifter of the present invention, a phase shifter 2203+ is corresponding to ~~an eleventh~~ a fifth phase shifter of the present invention, and a phase shifter 2203- is corresponding to a ~~twelfth~~sixth phase shifter of the present invention.

Please replace the paragraph, beginning at page 71, line 9, with the following rewritten paragraph:

FIG. 22 is the circuit diagram of the radio communication apparatus related to a sixth embodiment of the present invention. In FIG. 22, ~~an~~ antenna 1601+ is an example of a ~~fourth~~one antenna of the present invention, ~~an~~ antenna 1601- is an example of a ~~fifth~~another antenna of the present invention. A duplexer 1602 is an example of a ~~fourth~~ duplexer of the present invention comprising separate terminals, that is, the receiving output terminal is the single-phase output type, the transmission input terminal is the balanced input type, and the antenna input-output terminal is the balanced input-output type.

Please replace the paragraph, beginning at page 71, line 20, with the following rewriting paragraph:

As for the radio communication apparatus shown in FIG. 22, the radio frequency signal transmitted from the base station is received by the antennas 1601+ and 1601- as in FIG. 1 and is then inputted to the duplexer 1602. Furthermore, the signal outputted from the duplexer 1602 is inputted to the receiving circuit 1603 as an example of a ~~second~~ receiving apparatus of the present invention connected to said single-phase output terminal, where it is converted into the base band signal. The predetermined signal processing is performed to the transmitting base band signal and then it is inputted to a transmitting circuit 1604 as an example of a ~~third~~ transmitting apparatus of the present invention of outputting a transmitting signal as a differential signal, where it is converted into the radio frequency and amplified to the predetermined sending power to be inputted to the duplexer 1602. Furthermore, it is constituted so that this signal is outputted from the duplexer 1602 to be sent to the base station from the antennas 1601+ and 1601-. And a part of the transmitting signal inputted to the duplexer 1602 leaks to the receiving circuit 1603.

Please replace the paragraph, beginning at page 74, line 19, with the following rewritten paragraph:

FIG. 23 is a circuit diagram of the radio communication apparatus related to a seventh embodiment of the present invention. In FIG. 23, the same components as those shown in FIG. 22 are given the same symbols, and a description thereof will be omitted. In FIG. 23, ~~an~~ antenna 1701+ is another example of the ~~fourth~~said one antenna of the present invention and ~~an~~ antenna 1701- is another example of the ~~fifth~~said another antenna thereof. A duplexer 1702

is another example of the ~~fourth~~ duplexer of the present invention in which the transmission input terminal is the single-phase input type, the receiving output terminal is the balanced output type, and the antenna input-output terminal is the balanced input-output type.

Please replace the paragraph, beginning at page 77, line 23, with the following rewritten paragraph:

FIG. 25 is a circuit diagram of the radio communication apparatus related to an eighth embodiment of the present invention. In FIG. 25, an antenna 2301+ is an example of the ~~sixth~~one antenna of the present invention and an antenna 2301- is an example of the ~~seventh~~another antenna thereof. A duplexer 2302 is an example of the ~~fifth~~ duplexer of the present invention in which the receiving output terminal is the balanced input type, the transmission input terminal is the balanced output type, and the antenna input-output terminal is the balanced input-output type. Reference numeral 2303 denotes the receiving circuit of the differential input as another example of the ~~first~~ receiving apparatus of the present invention described in the first embodiment, and 2304 denotes the transmitting circuit of the differential output as an example of the ~~third~~ transmitting apparatus of the present invention described in the sixth embodiment.

Please replace the paragraph, beginning at page 80, line 18, with the following rewriting paragraph:

In FIG. 26, a phase shifter 2401+ is corresponding to a ~~thirteenth~~first phase shifter of the present invention, a phase shifter 2401- is corresponding to a ~~fourteenth~~second phase shifter of the present invention, a phase shifter 2402+ is corresponding to a ~~fifteenth~~third phase shifter of the present invention, a phase shifter 2402- is corresponding to a ~~sixteenth~~fourth phase shifter of the present invention, a phase shifter 2403+ is corresponding to a ~~seventeenth~~fifth phase shifter of the present invention, and a phase shifter 2403- is corresponding to ~~an eighteenth~~a sixth phase shifter of the present invention.

Please replace the paragraph, beginning at page 84, line 7, with the following rewriting paragraph:

The duplexer 3002 corresponding to a ~~sixth~~ duplexer of the present invention as an example plays a role of reducing the transmitting signal leak to the receiving circuit and also plays a role of preventing deterioration of the noise characteristic of the transmitting signal due

to a part of the receiving signal leaking to the transmitting circuit. FIG. 29 shows an example of the configuration of such a duplexer 3002. In FIG. 29, 1321+ and 1321- denote examples of a third band pass filter and a fourth band pass filter of the present invention. The transmitting signals outputted from the transmitting circuit 204 are distributed to the filters 1321+ and 1321-. The transmitting signals outputted from the filters 1321+ and 1321- are inputted to the antennas 1101+ and 1101- respectively.